

## Noise shaping structures of band pass $\Sigma\Delta$ ADC and their impact on metrological parameters.

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**Abstract-** The bandpass sigma delta analog-digital converters (BP  $\Sigma\Delta$  ADC) represent the unconventional ADCs, which are mostly embedded into digital communication systems or direct conversion of the voltage vector into the digital complex number. The structure of the noise shaping feedback impacts dominantly S/N ratio. The various feedback structures of the BP  $\Sigma\Delta$  ADC designed from their low-pass prototype will be presented in the paper. The impact of the proposed structures on the basic metrological parameters of the BP  $\Sigma\Delta$  ADC will be analyzed here.

### I. Introduction

The main advantage of a  $\Sigma\Delta$  ADC is flexibility of achieved resolution and noise shaping feedback structure. The simple circuit implementation with high degree of reprogrammability of its binary blocks is another advantage of this structure. The basic structure of the  $\Sigma\Delta$  modulator is transformable in the frequency domain. The most popular modification of basic low pass  $\Sigma\Delta$  ADC is the band pass  $\Sigma\Delta$  ADC.

The BP  $\Sigma\Delta$  ADCs are mostly embedded into digital communication systems (DCS) in various applications. Their main task is to provide a frequency down conversion along with the conversion of chosen parameter of analog signal into digit. These converters are analog front end for DCS like software radios, UMTS, GSM and GPS systems. Another application of the BP  $\Sigma\Delta$  ADC is targeted on the direct conversion of the complex harmonic signal at the input into the digital representation of its real and imaginary part. Such phase sensitive detection and analog-to-digital conversion is useful for processing signals from sensors where complex output impedance is modulated by two measured parameters mutually. High suppression of the quantisation noise around the central frequency is another advantage of implementation BP  $\Sigma\Delta$  ADC.

The noise suppression of LP  $\Sigma\Delta$  ADC depends on the noise shaping function. The increasing order of noise shaping structure is limited by stability constraints. The multiple noise shaping structures known as MASH  $\Sigma\Delta$  ADC is the way how to avoid problems with stability. While there are many papers proposing various modifications of the noise shaping structure with ensured stability, the BP  $\Sigma\Delta$  ADC are mostly based on the first order structure. Authors present the approach how to design various types of the noise shaping structures in the case of BP  $\Sigma\Delta$  ADC. Moreover the improvement in quantization noise suppression is derived and experimentally evaluated.

The transfer functions of basic  $\Sigma\Delta$  feedback structures of  $L$ th order is represented by the formula (1). Corresponding circuit realisations are on [3].

$$Y(z) = X(z) \cdot z^{-1} + Q(z)(1 - z^{-1})^L \quad (1)$$

The transfer function of the MASH ADC structure is characterized by the formula (2). The noise components  $Q_1, Q_2$  corresponds to the first and second comparator in the parallel structure. For transfer functions  $F_1 = z^{-1}$  and  $F_2 = (1 - z^{-1})$  the quantization noise impact is eliminated.

$$\begin{aligned} Y(z) &= z^{-1} \cdot F_1(z) \cdot X(z) + [F_1(z) \cdot (1 - z^{-1}) - F_1(z) \cdot z^{-1}] Q_1(z) + F_2(z) \cdot Q_2(z) \cdot (1 - z^{-1}) \\ &= \left. \begin{array}{l} F_1(z) = z^{-1} \\ F_2(z) = 1 - z^{-1} \end{array} \right| = X(z) z^{-1} + Q_2(z) \cdot (1 - z^{-1})^2 \end{aligned} \quad (2)$$

Any other type of the  $\Sigma\Delta$  modulator is achieved by the frequency transformation of the basic low pass transfer characteristic [3]. The frequency transformation from LP to BP is being performed by replacing of the term  $z^{-1}$  in the LP function by the  $(-z^{-2})$ . The obtained BP structure has doubled order and final transfer functions are

expressed by following equations.

$$Y(z) = -X(z) \cdot z^{-2} + Q(z)(1 + z^{-2})^L \quad (3)$$

$$Y_{MASH}(z) = -z^{-2} F_1(z) \cdot X(z) + [F_1(z) \cdot (1 + z^{-2}) + F_1(z) \cdot z^{-2}] Q_1(z) + F_2(z) \cdot Q_2(z) \cdot (1 + z^{-2}) \quad (4)$$

Corresponding circuits are shown on Fig.1. The integrator in the LP structure is being replaced by the resonator.

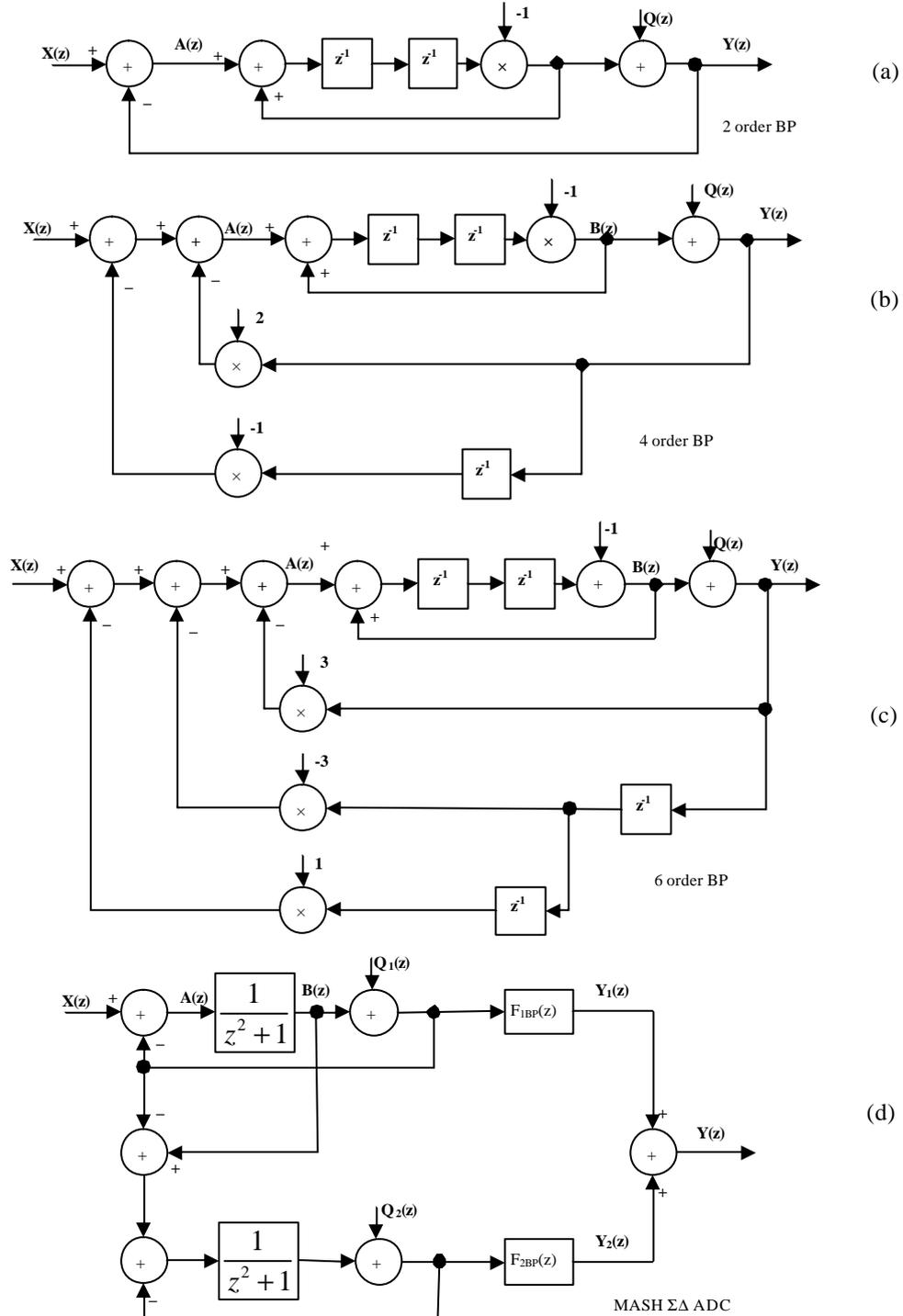


Figure 1. BP  $\Sigma\Delta$  ADC 2 (a), 4 (b), 6 (c) order and MASH  $\Sigma\Delta$  ADC (d)

## II. The quantization noise reduction

The effect gain of the noise suppression is being determined by the resolution gain  $R$  caused by the shaping feedback and low pass filtering. The total resolution is expressed by the formula:

$$R = \log_2 \frac{u_{q,NS}}{u_q} = \log_2 \frac{u_{q,IN}}{u_q} + \log_2 \frac{u_{q,NS}}{u_{q,IN}} = R1 + R2 \quad (5)$$

Where  $u_{q,IN}$  is effective quantization noise after noise filtering,  $u_{q,NS}$  is effective noise at the LP filter and  $u_q$  the same noise before filtering.

Let consider input signal with frequency band  $\Delta\omega = \frac{\omega_s}{2}$  around central frequency  $\omega_c = \frac{\omega_s}{4}$ .

Oversampling ratio  $OSR$   $\Delta\omega_{out} = \frac{\omega_s}{2 \cdot OSR}$  determines signal frequency range at the output of the BP  $\Sigma\Delta$  ADC. This frequency range determines the maximal modulating frequency carrying information.

Taking in the consideration the transfer functions (3) and (4) the spectral noise power density is determined by the formula:

$$S(\omega) = S_e(\omega) \cdot 2 \cdot [1 + \cos 2\omega T_s]^L = S_e(\omega) \cdot 4 \cdot \cos^{2L} \omega T_s \quad (6)$$

Where  $S_e$  is uniform noise power density of the quantizer.  $e_1^2 = \frac{Q}{6 \cdot \Delta\omega_s} = \frac{u_q^2}{\omega_s/2}$

The noise power  $u_{q,NS}$  after filtering by rectangular transfer function (Ideal Filter) in the frequency range  $\omega_c \pm \frac{\Delta\omega}{2}$  is

$$u_{q,NS}^2 = e_1^2 \cdot \frac{1}{T_s} \int_{\omega_c - \Delta\omega}^{\omega_c + \Delta\omega} 4^L \cdot \cos^{2L} \omega T_s d\omega T_s \quad (7)$$

The resolution gain R1 after noise shaping to the uniform quantization noise from the same frequency range could be calculated from the spectrum in the frequency domain.

$$R1 = -\frac{1}{2} \log_2 \frac{1}{\Delta\omega} \int_{\omega_c - \frac{\omega_s}{2}}^{\omega_c + \frac{\omega_s}{2}} 4^L \cdot \cos^{2L} \omega T_s d\omega \quad (8)$$

Values of R1 were calculated numerically (Table.1).

R1	OSR=4	OSR=16	OSR=64
L=1	0,3541	0,8607	1,4335
L=2	0,6060	1,6063	2,7114
L=3	0,8177	2,3095	3,9469

Table 1. Resolution gain  $R_1$

The analytical value  $R$  was compared using the simulation of BP

The noise suppression after averaging FIR filter (Av FIR) with length of  $OSR$  samples is represented

$$u_{q,NS}^2 = \frac{u_q^2}{f_s/2} \cdot \frac{4^L}{T_s} \int_{\omega_c - \frac{\omega_s}{2}}^{\omega_c + \frac{\omega_s}{2}} \frac{\omega T_s}{2} \sin^2 \left( \frac{\omega T_s}{2} \cdot OSR \right) d\omega T_s \quad (9)$$

The expression is being calculated numerically. Effect of different filters on the resolution gain  $R$  caused by the noise shaping  $R1$  and LP filtering  $R2$  from the output of the BP  $\Sigma\Delta$  modulator noise for parameter  $L=2$  is shown in the Figure 2. Various types of the LP filters e.g. FIR filter, Bessel, Chebyshev and Butterworth filters of different orders have been studied.

As shown on the figure the averaging comb filter is not as good as the others considered filters. From all considered filters, the Butterworth filter provides the best results.

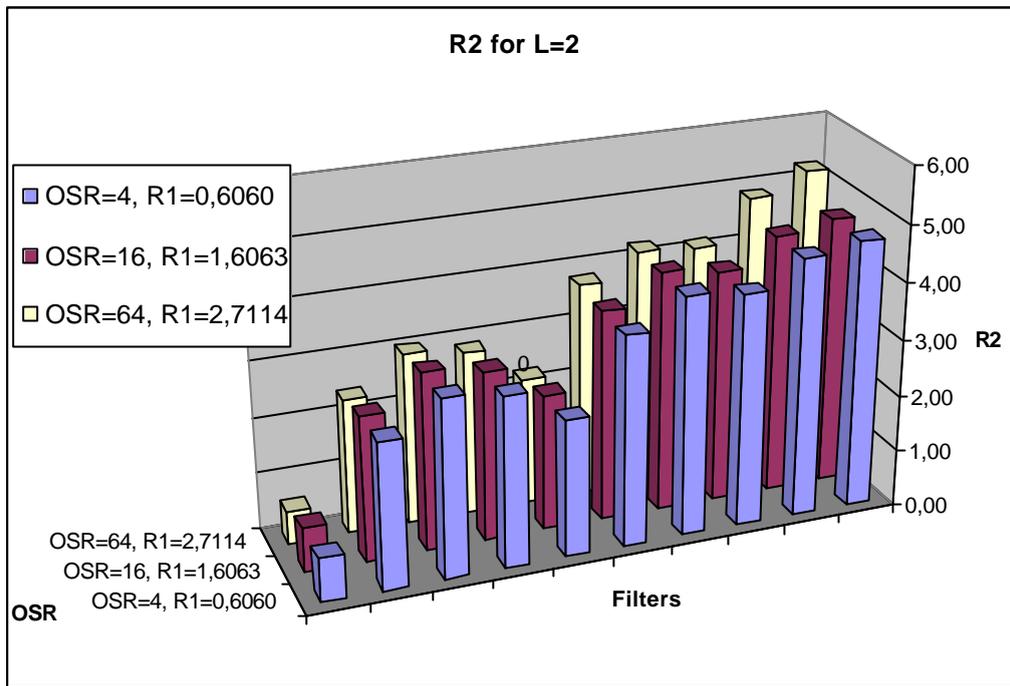


Figure 2. Resolution  $R$  for different filters

In the next graph (Fig. 3) is shown the effect of parameter  $M$  on  $R1$  and  $R2$  for 2. order Butterworth filter.

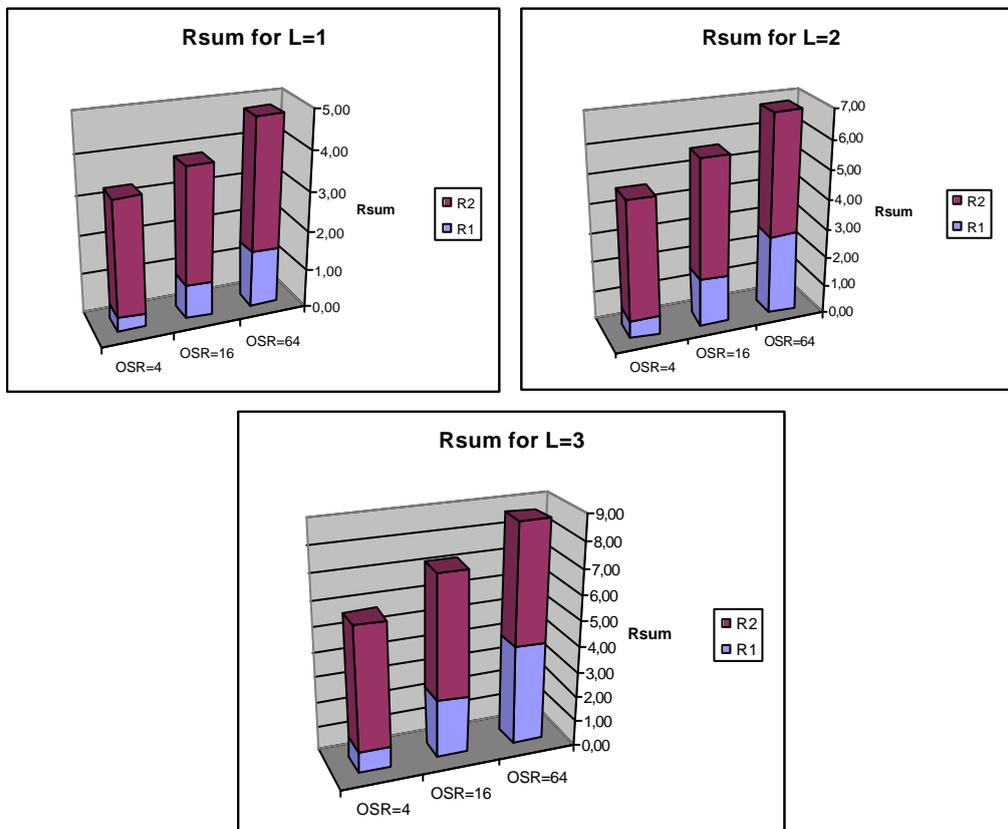


Figure 3.  $R1$  and  $R2$  for 2. order Butterworth filter

### III. Experimental results

The mathematical analysis from the previous paragraph was performed under some simplification. First one is the consideration that quantization noise is ideally white. Second consideration is ideal transfer function of the LP filters at the modulator output. Third one is the inherent noise of electronic components in the converter. Besides thermal and flicker noise the switching blocks contribute to the final noise dominantly.

The noise figures improvement of the proposed BP final  $\Sigma\Delta$  ADC has been studied using software simulations. The error model of the converter was developed in the LabVIEW environment. The noise suppression was calculated from the noise spectra for various orders of noise shaping feedback (Fig.4). The noise spectra at the output of the  $\Sigma\Delta$  modulator and  $\Sigma\Delta$  ADC are shown on Fig.4 a-c.

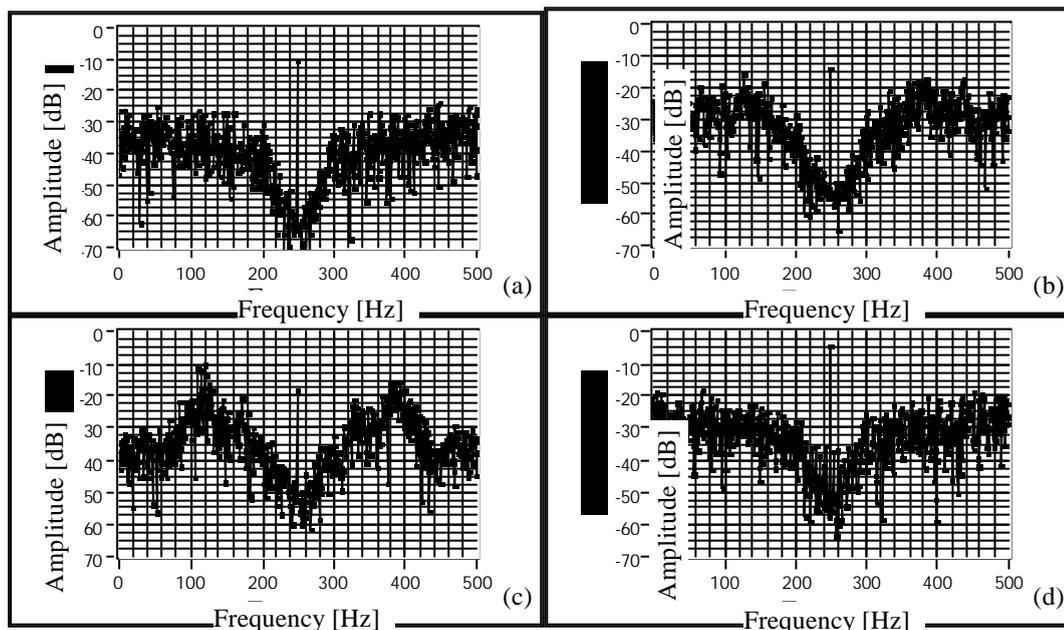


Figure 4. Power spectrum BP  $\Sigma\Delta$  2 (a), 4 (b), 6 (c) order and  $\Sigma\Delta$  MASH(d)

Authors has designed real BP  $\Sigma\Delta$  ADC on the programmable IC Cypress SoC . Its transfer function is shown on Fig.5. The difference to the ideal shape is caused by the additive switching noise. The final experimentally verified spectra shows satisfying conformity with the simulation results.

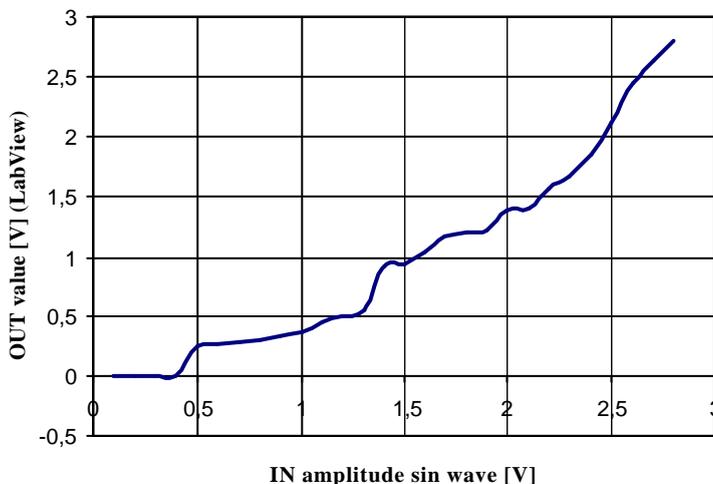


Figure 5. BP  $\Sigma\Delta$  ADC transfer function

#### IV. Conclusions

As shown from the formula (1) and (3) ideal noise shaping function around zero frequency in LP  $\Sigma\Delta$  ADC is the same as for the BP  $\Sigma\Delta$  ADC around the central frequency. For real converters differences in the parameter representing noise suppression are caused by the switching noise mainly.

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#### References

- [1] Boehm K., Hentschel, T., Mueller, T., Oehler, F., Rohmer, G.: An IF Digitizing Receiver for a Combined GPS/GSM Terminal Proceedings of conference RAWCON1
- [1] Haze, J., Vrba, R., Fucik, L., Forejtek, J., Zavoral, P., Pavlik, M., Michaeli, L., "BandPass Sigma-Delta Modulator for Capacitive Pressure Sensor", in Proceedings of IMTC 2007 Conference, Warsaw, Poland, ISBN 1-4244-0589-0
- [2] Ong, A. K., Wooley, B., A., "A Two-Path Bandpass SD Modulátor for Digital IF Extraction at 20 MHz", IEEE Journal of Solid-State Circuits, vol. 32, No. 12, December, 1997
- [3] Van van Plassche, R., J. CMOS Integrated Analog-to Digital Converters, Kluwer Acad. Publ. 2003
- [4] Tabatabaei, A., Wooley, B., A., "A Two-Path Bandpass Sigma-Delta Modulátor with Extended Noise Shaping", IEEE Journal of Solid-State Circuits, vol. 35, No. 12, December, 2000
- [5] Brigati, S., Francesconi, F., Malcovati, P., Maloberti, F., "A Fourth-Order Single-Bit Switched-Capacitor Sigma-Delta Modulator for Distributed Sensor Applications", IEEE Transactions on Instrumentation and Measurement, vol. 53, pp. 266-270, April 2004
- [6] Kulah, H., Chae, J., Yazdi, N., Najafi, K., "Noise analysis and characterization of a sigma-delta capacitive microaccelerometer, IEEE Journal of Solid-State Circuits, Volume 41, Issue 2, Feb. 2006 Page(s): 352 – 361
- [7] Schreirer, R., Temes, C., G., "Understanding Delta-Sigma Converters" IEEE Press, 2005